REMARKS

Claim 34 has been rewritten in independent form. Claims 14-38 remain in the application. Applicant reserves the right to pursue the original claims and other claims in this and other applications.

Claims 14-33 and 38 are rejected under 35 U.S.C. § 102(b) as being anticipated by Roth. Reconsideration is respectfully requested. Roth discloses a system for recording information Va on a record carrier 2. According to Roth, the recording process can be interrupted and resumed (column 2, lines 10+). According to the Office Action, during the interruption of the recording process, "data from a memory" (input buffer 6) is maintained (Office Action, page 2). The Office Action also points to column 6, lines 62-68 of Roth, which states as follows:

> ". . . The actual interruption of the recording is realized in step S8 [Fig. 2]. In this step, at the instant at which the position is reached where the recording is to be interrupted, the read/write head 3 is set to the read mode dn at the same time the transfer of the information from the input memory store 6 is discontinued [by deactivating the generation of the transfer clock signal cl3]."

Claims 14-31 relate to a method of recording data on an optical data recording media. Each claim recites the steps of "storing . . . encoded data in a device," and, during pausing, "maintaining said encoded information in said device." Roth fails to disclose or suggest the step of "maintaining said encoded information," and the Office Action provides no explanation to the contrary. The Office Action discusses the purported maintenance of data from the input buffer 6, but says nothing about the maintenance of encoded data as in the claimed invention. This is an important aspect of the claimed invention. Consequently, the rejection of claims 14-31 should be withdrawn.

Independent claim 32 relates to a circuit for encoding information to be written. According to claim 32, the circuit has "a buffer, coupled to [an] encoder, for storing encoded information," and "a controller . . . for pausing said encoder while maintaining the encoded information stored in said buffer." Roth fails to disclose or suggest a controller for pausing an encoder while "maintaining the encoded information . . . ." Consequently, the rejection of claim 32 should be withdrawn.

Claim 33 relates to a recording apparatus. The apparatus has "an encoding circuit . . . for storing encoded data in an encoding buffer," and "a pause circuit for pausing said encoding circuit while maintaining said encoded data in said encoding buffer." Roth fails to disclose or suggest the recited circuits. Consequently, the rejection of claim 33 should be withdrawn.

Claim 38 recites an encoding circuit for a CD-R or CD-RW recording apparatus. The claimed circuit has "a CIRC encoder for generating encoded data," and claim 38 says that the circuit "supports a pause condition which maintains [the encoded data stored in a memory]." Roth fails to disclose or suggest the recited maintenance of stored encoded data. Consequently, the rejection of claim 38 should be withdrawn, and there are other reasons why the rejection based on Roth should be withdrawn.

The allowance of claims 34-37 is gratefully acknowledged.

In view of the above, each of the presently pending claims in this application is believed to be in immediate condition for allowance. Accordingly, the Examiner is respectfully requested to pass this application to issue.

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Respectfully submitted,

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